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8mAD3484F



Specifications and Applications Information

09/24/08

Preliminary

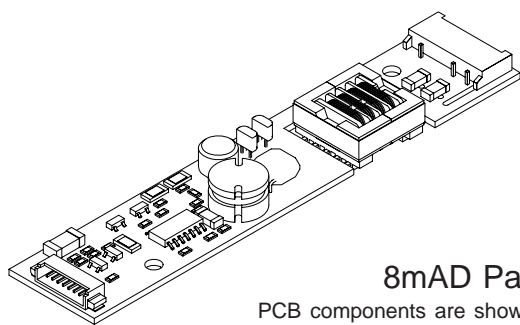
The ERG 8mAD3484F (8m Series) DC to AC inverter features onboard connectors and can be easily dimmed using an external analog control signal or external PWM generator.

Powered by a regulated +5 Volt DC source, the 8mAD3484F is designed to power the NEC NL6448BC33-63D display backlight.

Product Features

- ✓ Small Package Size, less than 9mm in height.
- ✓ High Dimming Ratio
- ✓ High Efficiency
- ✓ Made in U.S.A.

This unit complements our 8m Series of DC to AC Inverters



8mAD Package

PCB components are shown for reference only.
Actual product may differ from that shown.

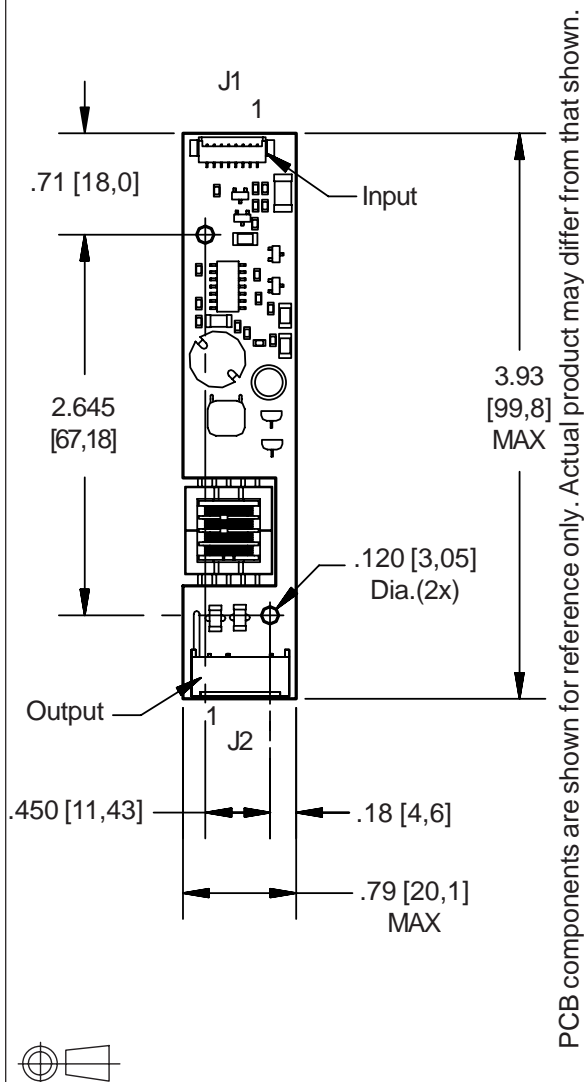
Connectors

Input J1
Molex 53261-0871

Output J2
JST SM04(4.0)B-BHS-1-TB

Two Lamp
DC to AC Inverter

Package Configuration



Pin Descriptions

J1-1 +Vin	J2-1 ACout
J1-2 +Vin	J2-2 ACout
J1-3 GND	J2-3 N/C
J1-4 GND	J2-4 ACreturn
J1-5 Enable	
J1-6 Control	
J1-7 N/C	
J1-8 GND	



Absolute Maximum Ratings

Rating	Symbol	Value	Units
Input Voltage Range	V_{in}	-0.3 to +5.5	Vdc
Storage Temperature	T_{stg}	-40 to +85	°C

Operating Characteristics

With a load simulating the referenced display and lamp warm-up of 5 minutes.
Unless otherwise noted $V_{in} = 5.00$ Volts dc and $T_a = 25^{\circ}\text{C}$.

Characteristic	Symbol	Min	Typ	Max	Units
Input Voltage	V_{in}	+4.75	+5.00	+5.25	Vdc
Component Surface Temperature ^(note 1)	T_s	-20	-	+80	°C
Input Current ^(note 2)	I_{in}	-	1.33	1.53	Adc
Input Ripple Current	I_{rip}	-	20	-	mA _{pk-pk}
Operating Frequency	F_o	32	37	42	kHz
Minimum Output Voltage ^(note 3)	$V_{out} \text{ (min)}$	1400	-	-	Vrms
Efficiency	h	-	77	-	%
Output Current (per lamp)	I_{out}	-	5.0	-	mArms
Output Voltage	V_{out}	-	510	-	Vrms
Enable Pin ^(note 4)					
Turn-off Threshold	V_{thoff}	GND	-	0.5	Vdc
Turn-on Threshold	V_{thon}	2.5	-	V_{in}	Vdc
Impedance to V_{in}	R_{Enable}	-	47	-	kOhms

Specifications subject to change without notice.

(Note 1) Surface temperature must not exceed 80 degrees C; thermal management actions may be required.

(Note 2) Input current in excess of maximum may indicate a load/inverter mismatch condition, which can result in reduced reliability. Please contact ERG technical support.

(Note 3) Provided data is not tested but guaranteed by design.

(Note 4) The inverter is always enabled with an internal pullup resistor tied to the enable pin. A ground on the enable input will turn the inverter off.

Application Notes:

- 1) The minimum distance from high voltage areas of the inverter to any conductive material should be .12 inches per kilovolt of starting voltage.
- 2) Mounting hardware to be non-conductive.
- 3) Open framed inverters should not be used in applications at altitudes over 10,000 feet.
- 4) Contact ERG for possible exceptions.



Onboard PWM

Unless otherwise noted $V_{in} = 5.00$ Volts DC, $T_a = 25$ °C and unit has been running for 5 minutes.

Characteristic	Symbol	Min	Typ	Max	Units
Frequency	f_{pwm}	-	160	-	Hz
Control Input Bias Current	I_{cbias}	-	-	10	μA

Pin Descriptions

Vin Input voltage to the inverter.

GND Inverter ground.

Control Analog voltage input to the onboard pulse width modulator. Increasing this voltage increases the off time of the onboard PWM resulting in decreased brightness. The inverter is full on when this voltage is near inverter ground.

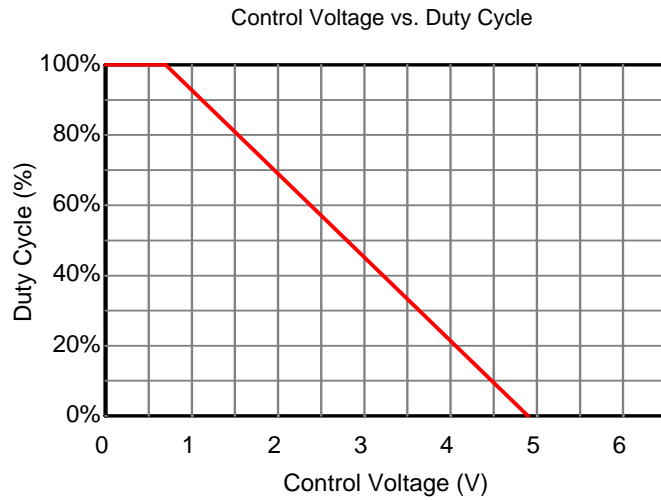
Enable Inverter Enable. The inverter is always enabled with an internal pullup resistor tied to the enable pin. Pull this pin low to disable inverter operation. The onboard PWM is always utilized.

Application information

The 8mAD series of inverters is designed to power two cold cathode fluorescent lamps each with four watts. An external analog control interfaces with an onboard pulse width modulator to provide dimming control. The 8mAD inverter can reliably dim to less than 5% duty cycle.

External shutdown of the inverter is accomplished using the Enable pin. Pulling this pin low (below V_{thoff}) disables the inverter.

Figure 1 shows the necessary connections for dimming control. Mechanical or digital potentiometers, as well as digital to analog converters, can be used to supply the dimming control voltage. Graph 1 shows the relationship of PWM duty cycle to input control voltage.



Graph 1

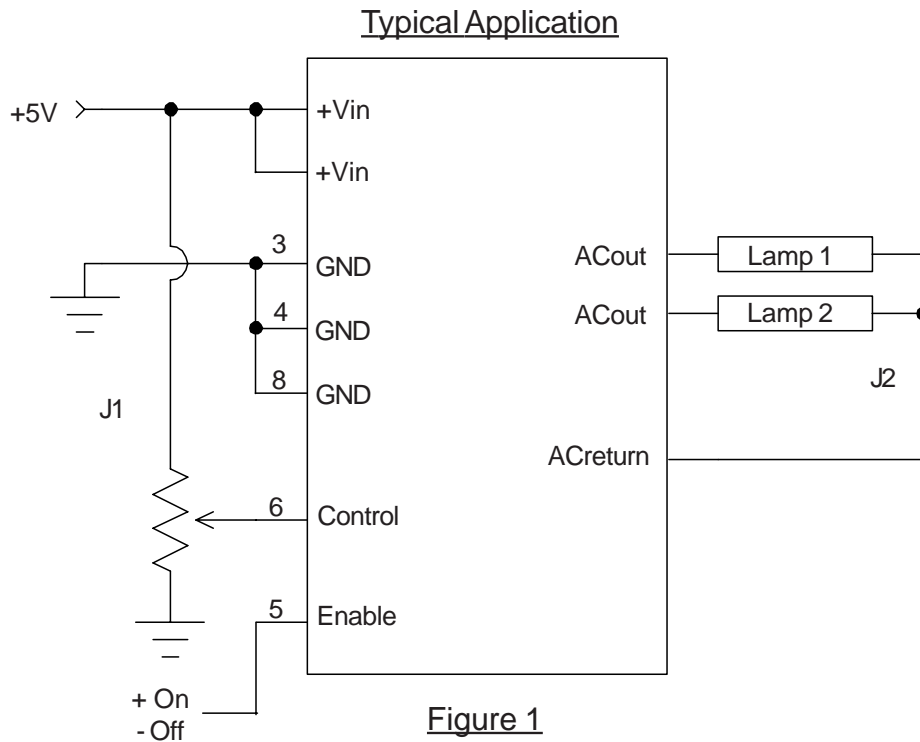


Figure 1



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