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8mAD3485F



Specifications and Applications Information

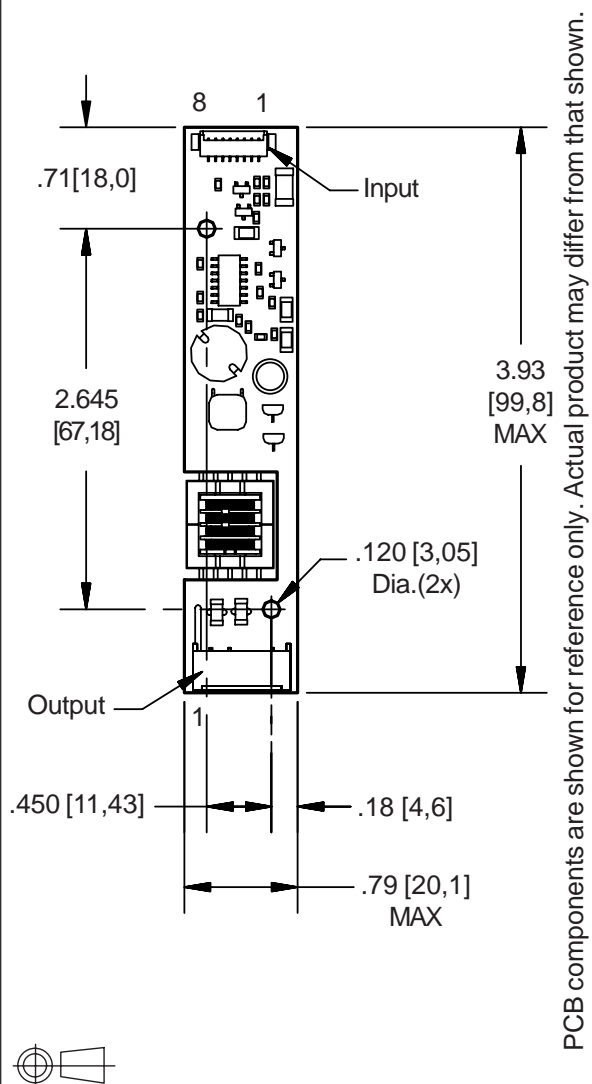
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The ERG 8mAD3485F (*8m Series*) DC to AC inverter features onboard connectors and can be easily dimmed using an external analog control signal or external PWM generator.

Powered by a regulated +12 Volt DC source, the 8mAD3485F is designed to power NEC NL6448BC33-63D display backlight.

Two Lamp
DC to AC Inverter

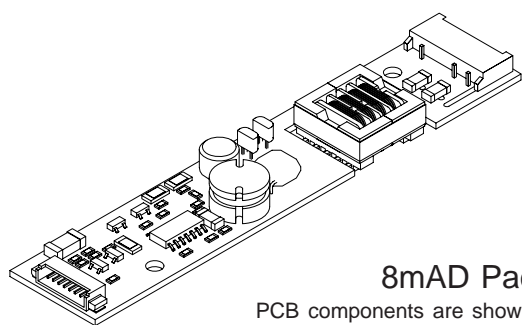
Package Configuration



Product Features

- ✓ Small Package Size, less than 9mm in height.
- ✓ High Dimming Ratio
- ✓ High Efficiency
- ✓ Made in U.S.A.

This unit complements our 8m Series of DC to AC Inverters



8mAD Package

PCB components are shown for reference only.
Actual product may differ from that shown.

Pin Descriptions

J1-1 Vin	J2-1 ACout
J1-2 Vin	J2-2 ACout
J1-3 GND	J2-3 N/C
J1-4 GND	J2-4 ACcommon
J1-5 Enable	
J1-6 Control	
J1-7 N/C	
J1-8 GND	

Connectors

Input J1
Molex 53261-0871

Output J2
JST SM04(4.0)B-BHS-1-TB



Absolute Maximum Ratings

Rating	Symbol	Value	Units
Input Voltage Range	V _{in}	-0.3 to +12.6	Vdc
Storage Temperature	T _{stg}	-40 to +85	°C

Operating Characteristics

With a load simulating the referenced display and lamp warm-up of 5 minutes.
Unless otherwise noted V_{in} = 12.00 Volts dc and T_a = 25°C.

Characteristic	Symbol	Min	Typ	Max	Units
Input Voltage	V _{in}	+10.8	+12.0	+12.6	Vdc
Component Surface Temperature	T _s	-20	-	+80	°C
Input Current (Note 1)	I _{in}	-	0.50	0.57	Adc
Input Ripple Current	I _{rip}	-	20	-	mA _{pk-pk}
Operating Frequency	F _o	41	46	51	kHz
Minimum Output Voltage	V _{out} (min)	1300	-	-	Vrms
Efficiency	η	-	86	-	%
Output Current (per lamp)	I _{out}	-	5.0	-	mArms
Output Voltage	V _{out}	-	510	-	Vrms
Enable Pin (Note 2)					
Turn-off Threshold	V _{thoff}	0	-	0.8	Vdc
Turn-On Threshold	V _{thon}	2.5	-	V _{in}	Vdc
Impedance to V _{in}	R _{Enable}	44.6	47	49.4	kOhms

(Note 1) Reliable and predictable operation of the device is not guaranteed with applied stresses at or beyond those listed in "Absolute Maximum Ratings". Operation at these limits may reduce device reliability and is therefore not recommended. Please refer to "Recommended Operating Conditions" for reliable operation of the device.

(Note 2) Reliable operation above 50°C is possible if airflow is provided.

(Note 3) When powering a load simulating the referenced display

(Note 4) The inverter is always enabled with an internal pullup resistor tied to the enable pin. A ground on the enable input will turn the inverter off.



Onboard PWM

Unless otherwise noted $V_{in} = 12.00$ Volts DC, $T_a = 25$ °C and unit has been running for 5 minutes.

Characteristic	Symbol	Min	Typ	Max	Units
Frequency	f_{pwm}	-	160	-	Hz
Control Input Bias Current	I_{cbias}	-	-	10	μA

Pin Descriptions

Vin Input voltage to the inverter.

GND Inverter ground.

Control Analog voltage input to the onboard pulse width modulator. Increasing this voltage increases the off time of the onboard PWM resulting in decreased brightness. The inverter is full on when this voltage is near inverter ground.

Enable Inverter Enable. The inverter is always enabled with an internal pullup resistor tied to the enable pin. Pull this pin low to disable inverter operation. The onboard PWM is always utilized.

Application information

The 8mAD series of inverters is designed to power two cold cathode fluorescent lamps each with four watts. An external analog control interfaces with an onboard pulse width modulator to provide dimming control. The 8mAD inverter can reliably dim to less than 5% duty cycle.

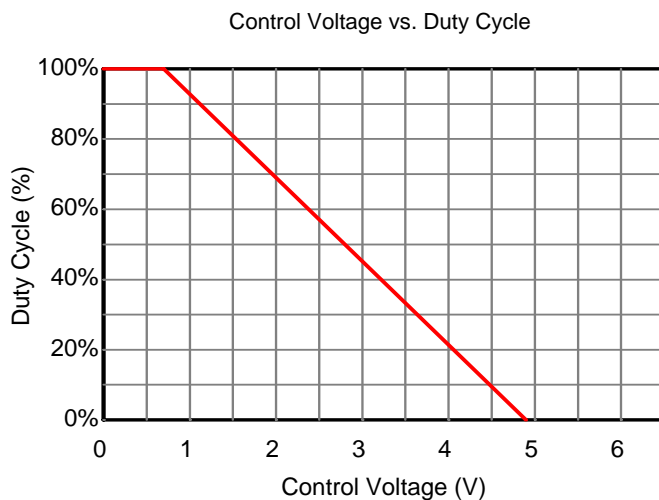
External shutdown of the inverter is accomplished using the Enable pin. Pulling this pin low (below V_{thoff}) disables the inverter.

If analog voltage dimming is required, the analog voltage is applied to the Control pin. Figure 1 shows how to connect the inverter for onboard PWM operation. Graph 1 shows the relationship of PWM duty cycle to input control voltage.

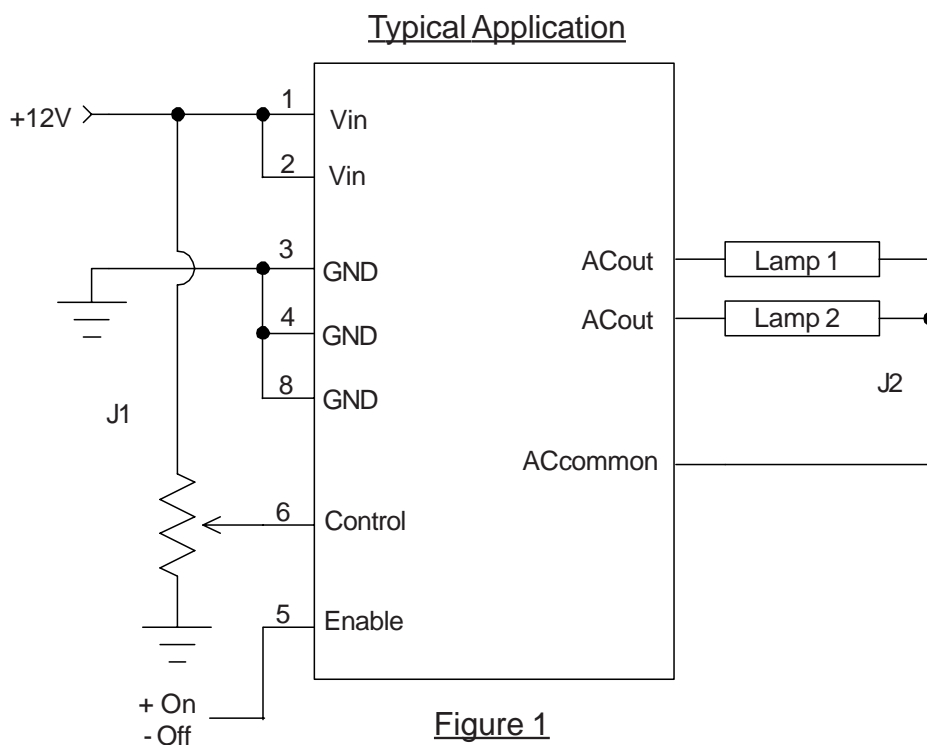
If an external PWM is used, simply connect the Enable pin to the PWM source and connect the Control pin to inverter ground. If the onboard PWM is used, connect the analog voltage to the Control pin.

Application Notes:

- 1) The minimum distance from high voltage areas of the inverter to any conductive material should be .12 inches per kilovolt of starting voltage.
- 2) Mounting hardware should be non-conductive.
- 3) Open framed inverters should not be used in applications at altitudes over 10,000 feet.
- 4) Contact ERG for possible exceptions.



Graph 1



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