Unit: mm

TOSHIBA Transistor Silicon-Germanium NPN Epitaxial Planar Type

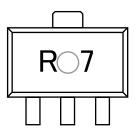
# MT3S113P

VHF-UHF Band Low-Noise, Low-Distortion Amplifier Applications

#### FEATURES

- Low Noise Figure:NF = 1.15dB (typ.) (@ f=1GHz)
- High Gain:|S21e|<sup>2</sup> = 10.5dB (typ.) (@ f=1GHz)

#### Marking

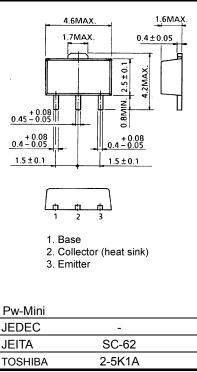


#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-emitter voltage	V <sub>CES</sub>	13	V
Collector-emitter voltage	V <sub>CEO</sub>	5.3	V
Emitter-base voltage	V <sub>EBO</sub>	0.6	V
Collector current	Ι <sub>C</sub>	100	mA
Base current	Ι <sub>Β</sub>	10	mA
Collector power dissipation	P <sub>C</sub> (Note1)	1.6	W
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	–55 to 150	°C

Note1:The device is mounted on a ceramic board (25.4 mm x 25.4 mm x 0.8 mm (t))

Note2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 0.05 g (typ.)

### Microwave Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Transition frequency	f <sub>T</sub>	V <sub>CE</sub> = 5V, I <sub>C</sub> = 50mA	5.5	7.7	_	GHz
Insertion gain	S21e  <sup>2</sup> (1)	V <sub>CE</sub> = 5V, I <sub>C</sub> = 50mA, f = 500MHz	_	16	_	dB
	S21e  <sup>2</sup> (2)	V <sub>CE</sub> = 5V, I <sub>C</sub> = 50mA, f = 1GHz	8.5	10.5	_	dB
Noise figure	NF(1)	V <sub>CE</sub> = 5V, I <sub>C</sub> = 50mA, f = 500MHz	_	0.95	_	dB
	NF(2)	V <sub>CE</sub> = 5V, I <sub>C</sub> = 50mA, f = 1GHz	_	1.15	1.45	dB
3 <sup>rd</sup> order intermodulation distortion output intercept point	OIP3	V <sub>CE</sub> = 5V, I <sub>C</sub> = 50mA, f = 500MHz, ⊿f=1MHz	32.5	36.7		dBmW

#### Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> = 5V, I <sub>E</sub> = 0	_	_	0.1	μA
DC current gain	h <sub>FE</sub>	V <sub>CE</sub> = 5V, I <sub>C</sub> = 30mA	200	_	400	—
Output capacitance	C <sub>ob</sub>	V <sub>CB</sub> = 5V, I <sub>E</sub> = 0, f = 1MHz	_	1.65	_	pF
Reverse transfer capacitance	C <sub>re</sub>	V <sub>CB</sub> = 5V, I <sub>E</sub> = 0, f = 1MHz (Note3)		1.25	1.55	pF

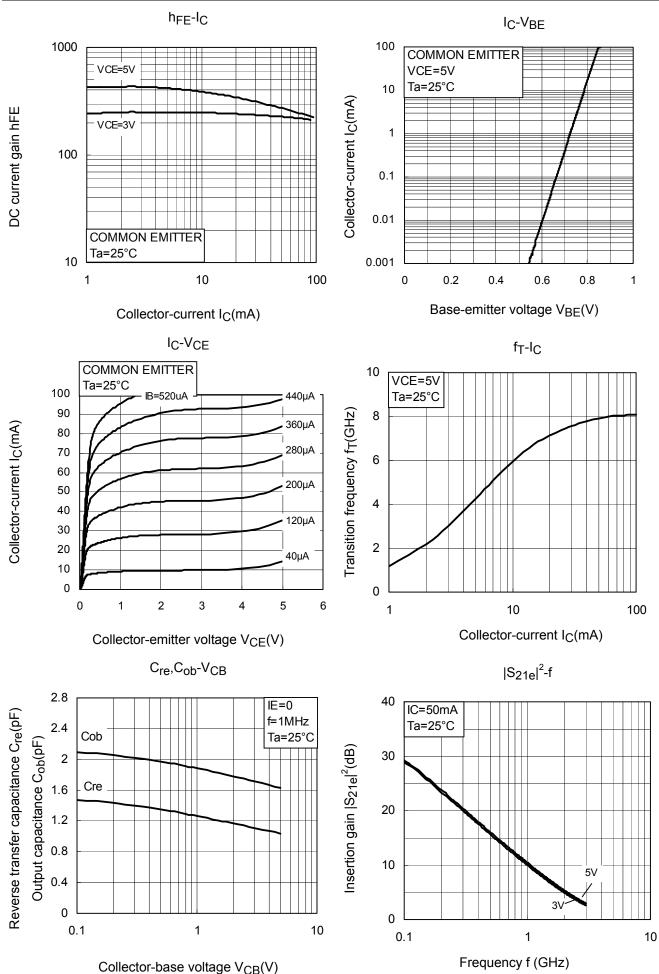
Note  $3:C_{re}$  is measured using a 3-terminal method with capacitance bridge

#### Caution:

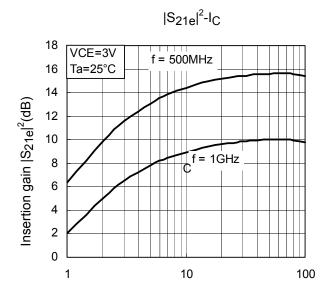
This device is sensitive to electrostatic discharge due to the high frequency transistor process of  $f_T$ =60GHz class is used for this product.

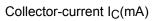
Please make enough tool and equipment earthed when you handle.

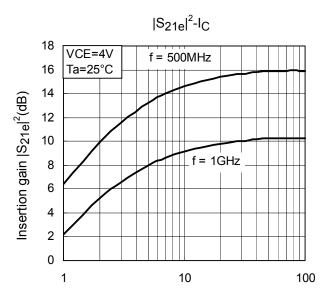
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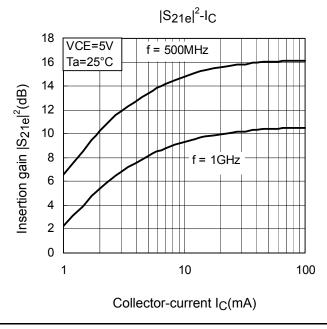
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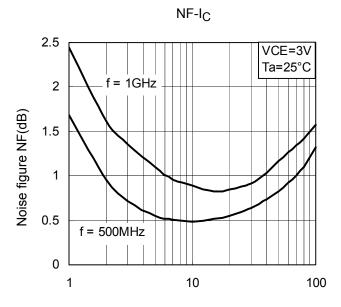




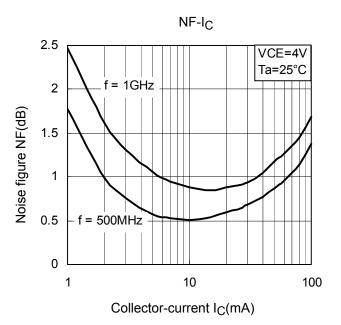


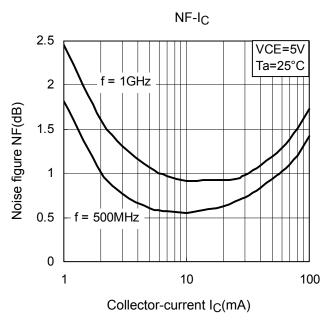
Collector-current I<sub>C</sub>(mA)



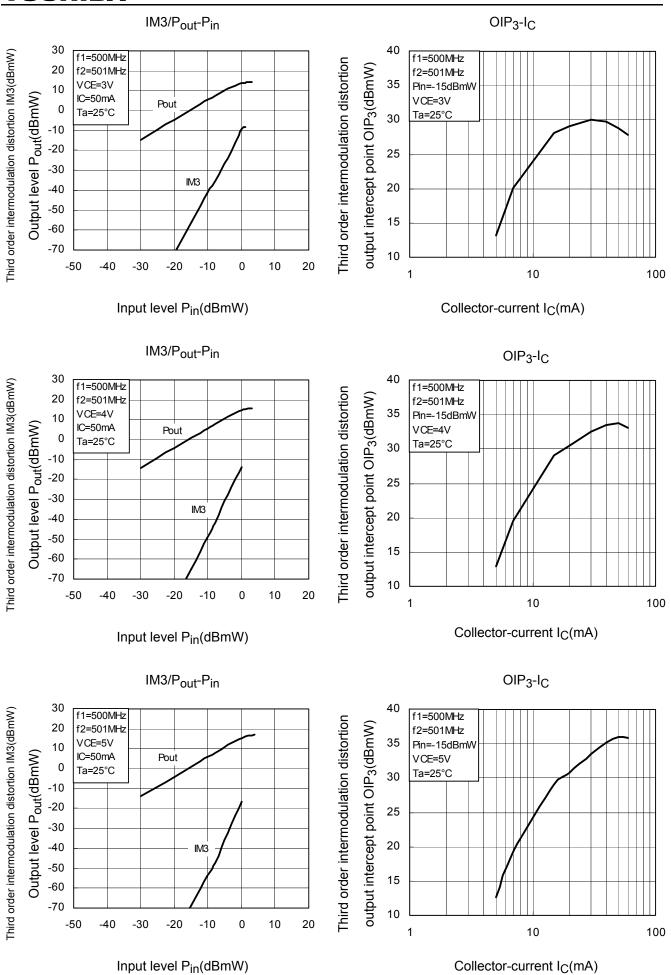


Collector-current I<sub>C</sub>(mA)

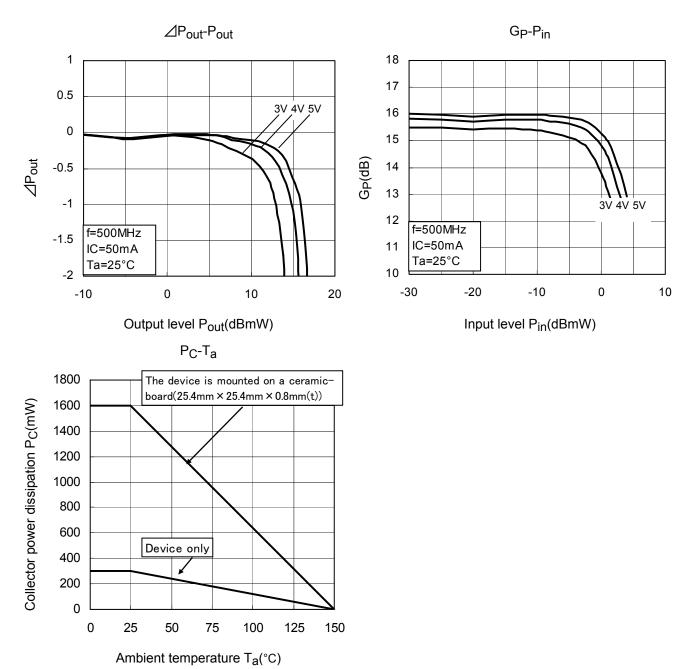




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